Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4697	((watch$1dog) adj timer)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 13:31
L2	93	l1 and (in$1circuit adj emulat$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:01
L3	85	l2 and clock	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:04
L4	28	l3 and sleep	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:03
L5	0	l4 and lock$1step$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:04
L6	0	l2 and lock$1step$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 13:48
L7	15	l1 and lock$1step$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 13:48
L8	12	l2 and (sleep adj mode)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 13:55
L9	31	l2 and ((sleep adj mode) or (standby))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 13:55
L10	451	l1 and ((in$1circuit adj emulat$5) or ICE or FPGA or (field adj programmable))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:21
L11	82	l10 and sleep	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:03
L12	331	l10 and (sleep or stall or wait or standby)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:03

L13	80	l11 and clock	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:04
L14	292	l12 and clock	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:18
L15	3	l14 and lock$1step$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:19
L16	78	l12 and (clock with (off or shut or cease or disconnect))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:19
L17	2	l16 and lock$1step$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:20
L18	1	"6393582".pn.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:20
L19	0	l18 and ((in$1circuit adj emulat$5) or ICE or FPGA or (field adj programmable))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 14:21
S1	6444	(sleep or stall or wait) adj (operation or command or instruction or function)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/25 13:30
S2	376	S1 and ("DUT" or (device adj under adj test) or "FPGA" or emulator)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:30
S3	284	S2 and clock	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:30
S4	26	S3 and lock$1step	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:30
S5	26	S4 and register	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:47

S6	26	S5 and (processor or cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:48
S7	393	S1 and ("DUT" or (device adj under adj test) or "FPGA" or emulator or (field adj programmable adj gate))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:47
S8	294	S7 and clock	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:47
S9	294	S8 and clock	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:47
S10	26	S9 and lock$1step	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:47
S11	26	S10 and register	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:47
S12	26	S11 and (processor or cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:57
S13	6	S12 and ((sleep or stall) adj (operation or command or instruction or function))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:57
S14	26	S10 and (processor or cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:57
S15	6	S14 and ((sleep or stall) adj (operation or command or instruction or function))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 20:58
S16	1	NEMECEK-CRAIG.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:14
S17	1	"5594741".PN.	USPAT; USOCR	OR	ON	2005/06/24 21:23
S18	1	"6466898".PN.	USPAT; USOCR	OR	ON	2005/06/24 21:23

S19	20314	(sleep$3 or stall$3 or wait$3) adj (operation or command or instruction or function or mode)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:29
S20	954	S19 and ("DUT" or (device adj under adj test) or "FPGA" or emulator)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:30
S21	679	S20 and clock	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:30
S22	30	S21 and lock$1step	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:31
S23	4	S22 not S14	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:33
S24	1	"6466898".pn. and (wait$3 or sleep$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/06/24 21:33

Results for "fpga<and>sleep<and>lockstep"
Your search matched 0 of 1174497 documents.
A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» View Session History

» New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

fpga<and>sleep<and>lockstep

Check to search only within this results set

Display Format: Citation Citation & Abstract

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revisir

Nothing Found

Your search for **+fpga +sleep +"lock step"** did not return any results.

You may want to try an Advanced Search for additional options.

Please review the Quick Tips below or for more information see the Search Tips.

Quick Tips

- Enter your search terms in lower case with a space between the terms.

  ```
  sales offices
  ```

  You can also enter a full question or concept in plain language.

  ```
  Where are the sales offices?
  ```

- Capitalize proper nouns to search for specific people, places, or products.

  ```
  John Colter, Netscape Navigator
  ```

- Enclose a phrase in double quotes to search for that exact phrase.

  ```
  "museum of natural history" "museum of modern art"
  ```

- Narrow your searches by using a + if a search term must appear on a page.

  ```
  museum +art
  ```

- Exclude pages by using a - if a search term must not appear on a page.

  ```
  museum -Paris
  ```

Combine these techniques to create a specific search query. The better your description of the information you want, the more relevant your results will be.

```
museum +"natural history" dinosaur -Chicago
```

Nothing Found

Your search for **+fpga +sleep +lockstep** did not return any results.

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Results for **"watchdog timer<and>sleep<and>fpga"**

e-mail

Your search matched **1** of **1174497** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» View Session History

» New Search

Modify Search

watchdog timer<and>sleep<and>fpga

Check to search only within this results set

Display Format: Citation Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

1. **Development of an innovative, two-processor data processing unit for the magne imaging instrument onboard the Cassini mission to Saturn. I. Hardware architect**
Psomoulis, A.M.; Cazajus, N.; Dandouras, D.S.; Barthe, H.; Gangloff, M.; Sarris, E.T.;
Geoscience and Remote Sensing, IEEE Transactions on
Volume 37, Issue 4, July 1999 Page(s):1980 - 1996
AbstractPlus | References | Full Text: PDF(716 KB) IEEE JNL

Nothing Found

Your search for **+"watchdog timer" +sleep +fpga** did not return any results.

You may want to try an Advanced Search for additional options.

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  ```

Combine these techniques to create a specific search query. The better your description of the information you want, the more relevant your results will be.

```
museum +"natural history" dinosaur -Chicago
```

Terms used **watchdog timer sleep fpga** Found **3** of **157,956**

Sort results by relevance

Display results expanded form

Save results to a Binder

Search Tips

Open results in a new window

Try an Advanced Search

Try this search in The ACM Guide

Results 1 - 3 of 3

Relevance scale

1 System architecture directions for networked sensors

Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister

November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Full text available: pdf(1.32 MB) Additional Information: full citation, abstract, references, citings, index terms

Technological progress in integrated, low-power, CMOS communication devices and sensors makes a rich design space of networked sensors viable. They can be deeply embedded in the physical world and spread throughout our environment like smart dust. The missing elements are an overall system architecture and a methodology for systematic advance. To this end, we identify key requirements, develop a small device that is representative of the class, design a tiny event-driven operating system, and sh ...

2 System architecture directions for networked sensors

Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister

November 2000 **Proceedings of the ninth international conference on Architectural support for programming languages and operating systems**, Volume 34 , 28 Issue 5 , 5

Full text available: pdf(299.01 KB) Additional Information: full citation, abstract, references, citings, index terms

Technological progress in integrated, low-power, CMOS communication devices and sensors makes a rich design space of networked sensors viable. They can be deeply embedded in the physical world and spread throughout our environment like smart dust. The missing elements are an overall system architecture and a methodology for systematic advance. To this end, we identify key requirements, develop a small device that is representative of the class, design a tiny event-driven operating system, and sh ...

3 Systems 1: Sensor network-based countersniper system

Gyula Simon, Miklós Maróti, Ákos Lédeczi, György Balogh, Branislav Kusy, András Nádas, Gábor Pap, János Sallai, Ken Frampton

November 2004 **Proceedings of the 2nd international conference on Embedded networked sensor systems**

Full text available: pdf(728.71 KB) Additional Information: full citation, abstract, references, citings, index terms

An ad-hoc wireless sensor network-based system is presented that detects and accurately locates shooters even in urban environments. The system consists of a large number of cheap sensors communicating through an ad-hoc wireless network, thus it is capable of tolerating multiple sensor failures, provides good coverage and high accuracy, and is capable of overcoming multipath effects. The performance of the proposed system is superior to that of centralized countersniper systems in such challe ...

Keywords: acoustic source localization, data fusion, message routing, middleware services, sensor networks, time synchronization

Results 1 - 3 of 3